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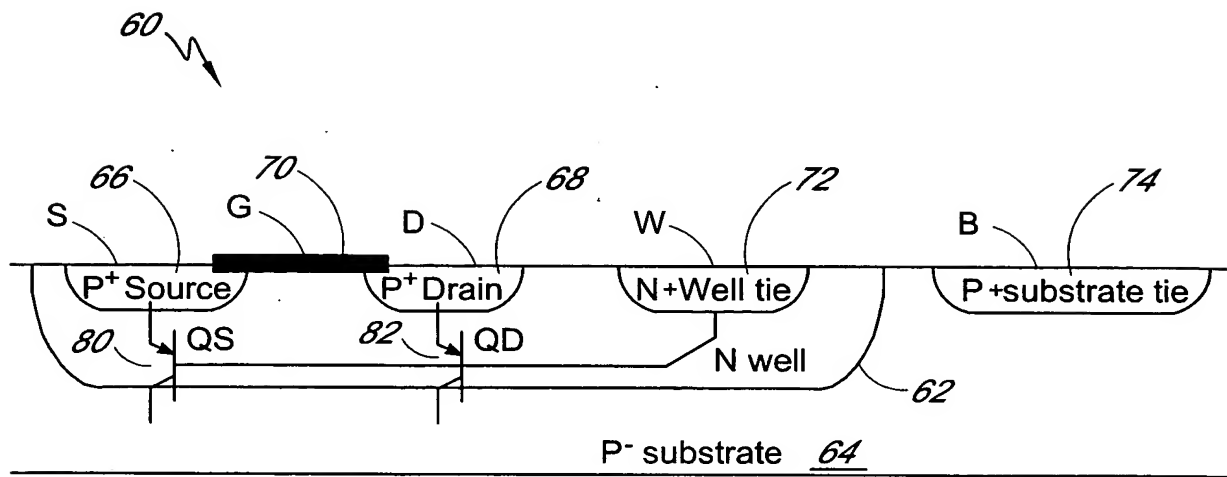


FIG. 1

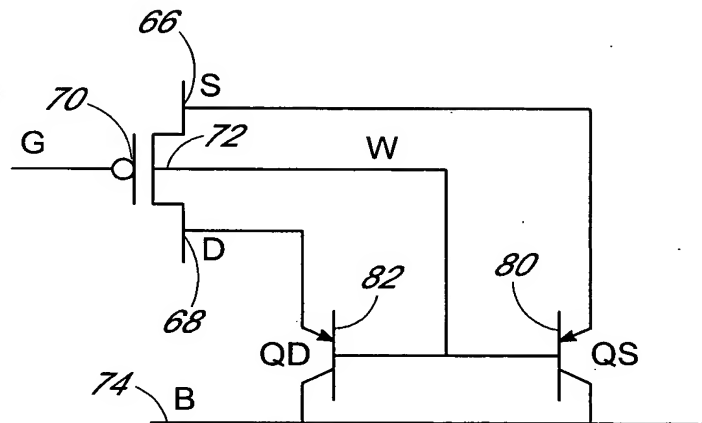


FIG. 2

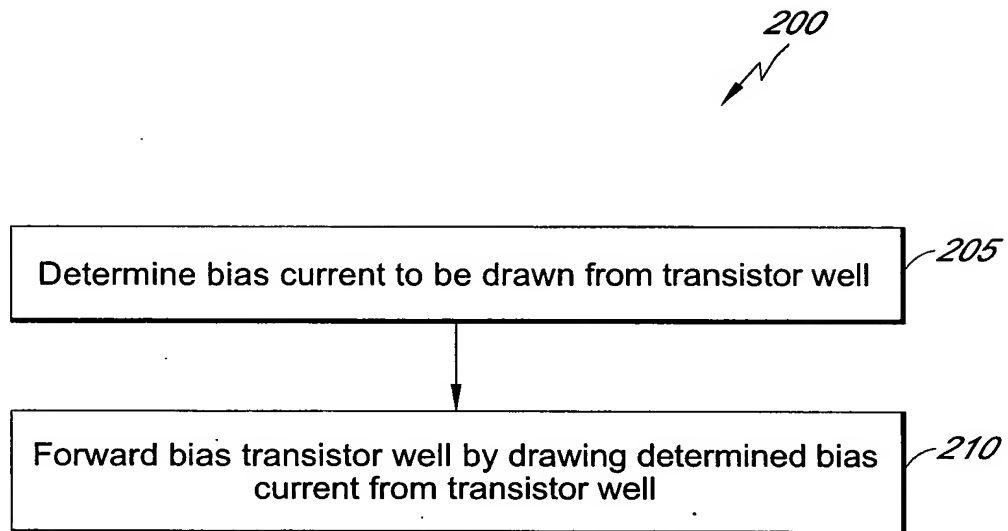


FIG. 3

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Appl. No.: unknown Atty Docket: ZLINK.026A

The circuit diagram shows a differential signal processing circuit. It includes several transistors labeled M1 through M9, two sense amplifiers QS_{M1} and QS_{M2}, and various interconnects labeled 100, 102, 110, 112, 116, 118, 120, 126, 128, 154, and 156. The circuit is powered by V_{dd} and V_{ss}. Input signals are V_{inp} and V_{inn}, and the output is V_{out}. A central horizontal line is labeled SUBSTRATE.

FIG. 6